

U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-9236
		Application No.: 10/660,169
OCT 8 2003 U.S. PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s): Robert D. Nuckolls, et al.
		Filing Date: 09-11-2003
		Group Art Unit: Not Yet Assigned
		Date Submitted: September 30, 2003
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
<i>h</i>	AA	HENNESSY, JOHN L. & PATTERSON, DAVID A., "Computer Architecture: A Quantitative Approach," Third Edition, 2003, Morgan Kaufmann Publishers, San Francisco, CA, pp. 196-215.
<i>h</i>	AB	SMITH, JAMES E., "A Study of Branch Prediction Strategies," Proc. Computer Architecture, 1981, pp. 135-148.
<i>h</i>	AC	YEH, T. & PATT, YALE N., "A Comparison of Dynamic Branch Predictors that use Two Levels of Branch History," Proceedings of the 20 th Annual International Symposium on Computer Architecture (ISCA 20), 1993, pp. 257-266.
<i>h</i>	AD	HILGENDORF, R. B., HEIM, G. J., ROSENSTIEL, W., "Evaluation of Branch-Prediction Methods on Traces from Commercial Applications," IBM J. Res. Develop., Vol. 43, No. 4, July 1999, pp. 579-593; http://research.ibm.com/journal/rd/434/hilgendorf.html .
<i>h</i>	AE	LEBECK, ALVIN R., "Lecture 7: Dynamic Branch Prediction, Superscalar, VLIW, and Software Pipelining," Computer Science 220, Fall 2001, pp. 1-22 (Slides 1-43); http://cs.duke.edu/education/courses/fall01/cps220/lectures/lect07-2up.pdf .
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Examiner	<i>h</i>	Date Considered <i>10/30/03</i>
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